DOCKET NO.: THOM-0038 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Zia Shlaimoun Confirmation No.: 5266
Application No.: 10/519,514 Group Art Unit: 2838

Filing Date: September 23, 2005 Examiner: Gary L. Laxton

For: METHOD AND DEVICE FOR CONTROLLING VOLTAGE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 CFR § 1.56 and in accordance with 37 CFR §§ 1.97-1.98, information relating to the above-identified application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 CFR § 1.56(b).

In accordance with § 1.97(b), since this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above identified application as set forth in § 1.491, before the mailing date of a first Office Action on the merits of the above-identified application, or before the mailing date of a first Office Action after the filing of request for continued examination under § 1.114, no additional fee is required.

steps of:

providing a device having an input which has an input voltage, the input being connected to an electrical supply having an alternating supply voltage;

an output having an output voltage;

comparing the output voltage with a predetermined voltage to generate a comparison signal; and

adjusting the output voltage in response to the comparison signal.

SU817675 discloses a voltage control device, wherein means to adjust the output voltage comprises means to adjust the delay of the onset of the rise of output voltage within at least a half-cycle. This device also provides a method, wherein adjustment of the output voltage comprises delaying the onset of the rise of output voltage within at least a half-cycle. Also, according to SU 817675, the means to delay includes a thyristor module comprising an antiparallel pair of thyristors. The device is powered by the input voltage.

SU688901 discloses a voltage control device, which is adapted to be connected to a multiple phase voltage, in particular, to a three phase voltage.

None of these documents teaches increasing or decreasing the output voltage in response to the comparison signal "whereby the output voltage is maintained at the predetermined voltage unless the variable alternating supply voltage falls below the predetermined voltage" as claimed.

Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050.

Date: March 30, 2007

Michael P. Dunnam Registration No. 32,611

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## Docket No. Application No. Form PTO-1449 Modified 10/519,514 THOM-0038 List of Patent and Publications Applicant Cited by Applicant Zia Shlaimoun (Use several sheets if necessary) U.S. Department of Commerce Filing Date Group Patent and Trademark Office September 23, 2005 2838 Confirmation No. 5266 **U. S. PATENT DOCUMENTS** Examiner **Document** Class **Subclass** Date Name Initial No. FOREIGN PATENT DOCUMENTS **Translation** Examiner Document No. Date Country YES NO Initial X 09/30/79 SU 5 688901 $\mathbf{X}$ 817675 03/30/81 SU 6 X 838669 06/15/81 SU **EXAMINER DATE CONSIDERED**